

# date executive committee



**General Chair**  
Rudy Lauwereins  
IMEC and KU Leuven, BE



**Vice General Chair**  
Donatella Sciuto  
Politecnico di Milano, IT



**Finance Chair & ICCAD rep**  
Norbert Wehn  
University of Kaiserslautern, DE



**Special Sessions Chair**  
Christian Piquet  
CSEM, CH



**Embedded Software Co-Chair**  
Joseph Sifakis  
VERIMAG, FR



**Test Co-Chair**  
Jaume Sigura  
University of Illes Balears, ES



**Exhibition Programme & Publicity Chair**  
Juergen Haase  
EDACentrum, DE



**System Design Records Chair**  
Giovanni de Micheli  
EPFL, CH



**Space and Aeronautics Day Chair**  
Sylvain Prudhomme  
Airbus, FR



**Ubiquitous Computing Day Chair**  
Petri Liiuha  
Nokia Research Center, FI



**Audio Visual Chair**  
Udo Keschull  
University of Heidelberg, DE



**Press Chair**  
Fred Santamaria  
Infotest, Paris, FR



**Awards Chair**  
Luca Benini  
University of Bologna, IT



**Travel Grants Chair**  
Marta Rencz  
TU Budapest, HU



**University Booth**  
Gilles Jacquemod  
University of Nice Sophia Antipolis, FR



**Vendors Committee Chair**  
Carsten Elgert  
Mentor Graphics, DE



**EDAA Liaison**  
Bernard Courtois  
TIMA Laboratory, FR



**EDAC Liaison & Audit Co-Chair**  
Robert Gardner  
EDA Consortium, US



**Programme Chair**  
Jan Madsen  
DTU, DK



**Vice Programme & Tutorials Chair**  
Zebo Peng  
University of Linkoping, SE



**Past General Chair & DATE rep at DAC**  
Georges Gielen  
KU Leuven, BE



**Friday Workshops Chair**  
Bashir Al-Hashimi  
University of Southampton, UK



**Embedded Software Co-Chair**  
Rolf Ernst  
TU Braunschweig, DE



**Test Co-Chair**  
Patrick Girard  
LIRMM, FR



**Applications Design Chair**  
Luca Fanucci  
University of Pisa, IT



**Interactive Presentations Chair**  
Ingrid Verbauwhede  
KU Leuven, BE



**Space and Aeronautics Day Chair**  
Erick Lansard  
Aleni, FR



**Electronic Review Chair**  
Wolfgang Mueller  
University of Paderborn, DE



**Web Chair**  
Fabrice Kordon  
UPMC, FR



**Proceedings Chair**  
Christophe Bobda  
University of Kaiserslautern, DE



**Fringe Meetings Chair**  
Gabriela Nicolescu  
Ecole Polytechnique de Montreal, CA



**Local Arrangements Chair**  
Cyril Luxey  
University of Nice, FR



**University Booth**  
Jean-Pierre Damiano  
University of Nice Sophia Antipolis, FR



**PCB Design Chair**  
Rainer Asfal  
Mentor Graphics, DE



**DAC rep at DATE**  
Ellen M Sentovich  
Cadence, US



**EDAC Liaison**  
Anne Cirkel  
Mentor Graphics, US



**ASPDAC rep at DATE**  
**Masahuri Imai**  
Osaka University, JP



**SAME Liaison**  
**Anne Claire Desneulin**  
SAME Association, FR



**Exhibition manager**  
**Emma Norton**  
EDA Exhibitions Ltd, UK



**Audit Chair**  
**Volker Dueppe**  
Munich, DE



**DATE rep at ASPDAC**  
**Wolfgang Nebel**  
Offis, Oldenberg, DE



**Event Director**  
**Andrew Porter**  
EDA Exhibitions Ltd, UK



**Conference Manager**  
**Sue Menzies**  
European Conferences, UK

## date sponsors committee



**Chair & EDAA Chair**  
**Berbard Courtois**  
TIMA Laboratory, FR



**IEEE Computer Society**  
**TW Williams**  
Synopsys, US



**RAS**  
**Alex Stempkovsky**  
Russian Academy of Science, RU



**ACM-SIGDA**  
**Robert A Walker**  
Kent State U, US



**EDA Consortium**  
**Robert Gardner**  
EDA Consortium, USA



**EDAA Finance Chair**  
**Herman Beke**  
ARM, BE



**IEEE Computer Society**  
**Yervant Zorian**  
Virage Logic, US



**ECSI**  
**Jean Mermet**  
ECSI, FR



**Audit Chair**  
**Volker Dueppe**  
Munich, DE



**EDA Consortium**  
**Anne Cirkel**  
Mentor Graphics, US

# technical programme topic chairs

## System Level Specification & Modeling

**Satnam Singh**  
Microsoft, US  
**Jan Oliver**  
Nokia,

## System Design Methods & Case Studies

**Donatella Sciuto**  
Politecnico di Milano, IT  
**Luciano Lavagno**  
Cadence, US

## System Synthesis and Optimisation

**Jurgen Teich**  
U of Erlangen, DE  
**Paul Pop**  
DTU, DK

## Simulation and Validation

**Mostapha Aboulhamid**  
U of Montreal, CA

**Franco Fummi**  
U of Verona, IT

## Design of Low Power Systems and Case Studies

**Miguel Miranda**  
IMEC, BE  
**Tajana Simunic**  
UC San Diego, US

## Power Estimation and Optimization

**Eike Schmidt**  
Chip Vision, DE  
**Dimitrios Soudris**  
U of Thrace, GR

## Innovative and Emerging Technologies, Systems and Applications

**Vijaykrishnan Narayanan**  
Pennsylvania State U, US  
**Christian Paulus**  
Siemens, DE

## Formal Verification

**Valeria Bertacco**  
U of Michigan, US  
**Roderick Bloem**  
TU Graz, AT

## Multi Processor & Network on Chip

**Luca Benini**  
U of Bologna, IT  
**Kees Goossens**  
NXP Research, NL

## Microarchitectural and Architectural Design

**Todd Austin**  
U of Michigan, US  
**Stamatis Vassiliadis**  
TU Delft, NL

## Architectural Synthesis

**Fabrizio Ferrandi**  
Politecnico di Milano, IT  
**Ryan Kastner**  
UC Santa Barbara, US

## Reconfigurable Computing

**Walid Najjar**  
UC Riverside, US  
**Wayne Luk**  
Imperial College, UK

## Synthesis for Deep-Submicron Circuits

**Tiziano Villa**  
Parades, IT  
**Jordi Cortadella**  
U Politecnica Catalonia, ES

## Physical Design and Verification

**Andreas Kuehlman**  
Cadence, US  
**Frank Johannes**  
TU Munich, DE

## Analogue & Mixed A/D Systems

**Angel Rodriguez-Vazquez**  
U of Sevilla, ES  
**Manfred Glesner**  
TU Darmstadt, DE

## Analogue+Mixed Signal Design, Symbolic Techniques

**Lars Hedrich**  
U of Hannover, DE  
**Gerd Vandersteen**  
IBM, DE

## Interconnect, EMC and Packaging Modeling

**Peter Feldmann**  
IBM TJ Watson Research, US  
**Luca Daniel**  
MIT, US

## Media and Signal Processing

**Marc Heijligers**  
NXP Semiconductors, NL  
**Andrea Giorgi**  
STMicroelectronics, IT

## Wireless Communication and Networking

**Cyprian Grassmann**  
Infineon, DE  
**Jeroen Vliegen**  
Texas Instruments, FR

## Automotive

**Luca Fanucci**  
Pisa U, IT  
**Annette Reutter**  
Bosch, DE

## Secure and Security Systems

**Marcello Coppola**  
STMicroelectronics, FR  
**Renaud Pacalet**  
GET / Telecom Paris, CNRS LTCI, FR

## Military, Space and Avionics Systems

**Bernard Candaele**  
Thales, FR  
**Philippe Manet**  
UCL U, Louvain-La-Neuve, BE

## Measurement and Instrumentation

**Romano Giannetti**  
U Pontificia Comillas, ES

## Bio-medical Systems

**Andre Immink**  
Philips, NL  
**Massimo Mischi**  
TU Eindhoven, NL

## System and Industrial Test

**Rainer Dorsch**  
IBM Deutschland Entwicklung, DE  
**Erik Larsson**  
Linkoping U, SE

## Design for Test and BIST

**Sybille Hellebrand**  
Paderborn U, DE  
**Alfredo Benso**  
Politecnico di Torino, IT

## Test Generation, Simulation and Diagnosis

**Matteo Sonza Reorda**  
Politecnico di Torino, IT  
**Bernd Becker**  
Freidburg U, DE

## On-Line Testing, Fault Tolerance, and Reliability

**Cecilia Metra**  
Bologna U, IT  
**Fabrizio Lombardi**  
Northeastern U, US

## Testing of Analog, Mixed-Signal, RF and Heterogeneous Circuits and Systems

**Abhit Chatterjee**  
Georgia Inst. of Technology, US  
**Salvador Mir**  
TIMA, FR

## Statistical, Physical, Defect-Based Testing and Test of Regular Structures

**Rob Aitken**  
ARM, US  
**Antonio Rubio**  
U P Catalonia, ES

## Real-Time Systems

**Gerhard Fohter**  
U Kaiserslautern, DE  
**Sanjoy Baruah**  
U of North Carolina, US

## Compilers, architectures, and software synthesis for embedded systems

**Hans van Someren**  
ACE, NL  
**Nigel Topham**  
U of Edinburgh, UK

## Model-based Design for Embedded Systems

**Janos Sztipanovits**  
Vanderbilt U, US  
**Pieter Mosterman**  
The MathWorks, US

## Middleware and Hardware-dependent software for embedded systems

**Sjir van Loo**  
Philips, NL  
**Harmke de Groot**  
European Microsoft Innovation Centre, DE

## System Design Records

**Pieter van der Wolf**  
NXP Research, NL  
**Juergen Becker**  
U Karlsruhe, DE