

VCDS Tool Demonstration

Rafael K. Morizawa, Kazuo Tanaka, Keisuke Watanabe, Yuji Kaitso,
Shoji Hanamura, Takao Shinsha, Michiaki Muraoka
Semiconductor Technology Academic Research Center (STARC)

Abstract

The VCore(*) based Design Methodology developed in the VCDS(**) project is a novel design methodology utilizing VCores. VCores are reusable functional cores defined at high level. We designed a SoC for Wearable Computer as a vehicle application in the pilot project (Figure 1), and compared our proposed methodology with a conventional RTL based design methodology by measuring the design productivity. We

obtained very promising prospects that the design productivity could be improved 20 times for the enhanced VCDS (Figure 2).

We will make a demonstration of the implemented pilot tool to show how effective our proposed methodology is.

(*) VCore: Virtual Core (**) VCDS: Virtual Core based Design System

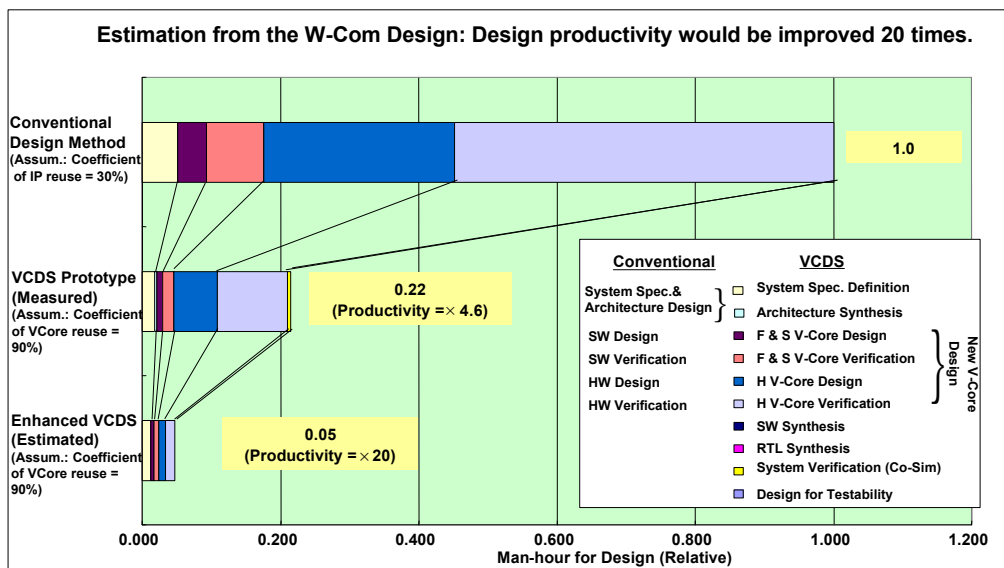
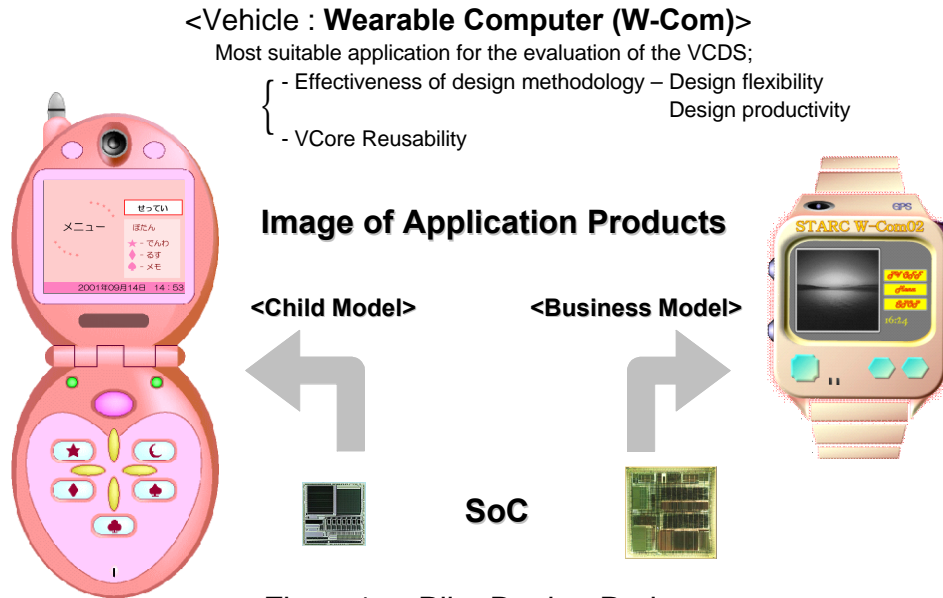


Figure 2. Effectiveness of VCore based Design - Productivity -