

CALL FOR PAPERS

The INTERNATIONAL CONFERENCE on CAD And DESIGN
ICCAD is oriented toward EDA and Design professionals,
concentrating on CAD and Design for Integrated Circuits and Systems.

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**AREAS OF INTEREST**

Original technical papers on (but not limited to) the following topics are invited:

1) PHYSICAL DESIGN AND TEST

- 1.1 Placement and floorplanning. Estimation techniques. Partitioning for layout.
- 1.2 Routing. Clock and global signal design.
- 1.3 Library design. Physical design planning and optimization. Layout verification. Layout for manufacturability. Sub-wavelength lithographic techniques.
- 1.4 Analog, RF, and mixed signal design and CAD. Mixed technology design (thermal, packaging, micro-mechanical).
- 1.5 Testing. Yield and manufacturability analysis. Fault modeling, delay test, analog and mixed signal test. Fault simulation. ATPG. BIST and DFT. Memory, core and system test.

2) SYNTHESIS AND SYSTEM DESIGN

- 2.1 Logic synthesis. Optimization for area, timing, power. FPGA design and optimization. Interaction between layout and logic synthesis. Technology mapping. Asynchronous circuit design.
- 2.2 Behavioral synthesis. Synthesis with IP libraries and reuse. Memory system synthesis. HW interface synthesis.
- 2.3 System synthesis (HW and SW). Hardware platform synthesis. ASIP synthesis. HW and SW estimation and analysis.
- 2.4 Embedded systems. Real-time software and RTOS. System level reuse techniques. Rapid system prototyping.

3) VERIFICATION, MODELING AND SIMULATION

- 3.1 Interconnect parameter extraction and circuit model generation.
- 3.2 Signal integrity analysis. Reliability analysis.
- 3.3 Gate, switch, and circuit level timing and power analysis.
- 3.4 Formal verification techniques. HW/SW co-simulation. Switch, logic and behavioral simulation and design validation.

4) INNOVATIVE CIRCUIT AND SYSTEM DESIGN AND DEVICES

- 4.1 New concepts and open problems in layout, circuit, logic and system design. SoC architectures including memory, processor, communication structures and programmable platforms.
- 4.2 CAD problems in emerging technologies. Modeling, simulation and analysis for new device structures. Single electron devices and circuits. Nanotechnology, MEMS, molecular and bioelectronics, optical and photonic devices and circuits.

AUTHOR INFORMATION AND FORMAT

One or more of the outstanding submissions will be recognized with the: **ICCAD William J. McCalla Best Paper Award**

Proposals for Panel Sessions and Tutorials are also invited.

Panel proposals should be sent to the Vice Chair.

Tutorial proposals should be sent to the Tutorial Chair.

AGAIN THIS YEAR: ICCAD submissions must be made electronically in PDF format through the ICCAD web site. Reference the ICCAD web page for instructions on electronic submissions: <http://www.iccad.com>.

Please submit the following in PDF format:

- **One pdf file which should contain the complete paper including the title and abstract, but excluding the authors' names and affiliations.** This file should be no more than 8 pages using proceedings format, double columned, 9pt or 10pt fonts including figures, tables and references. (In the proceedings, four pages are free of charge and each page beyond four pages is charged \$100.00 per page).
- **Papers in the wrong format, exceeding the eight page limit, or identifying the authors or their affiliations will be rejected immediately.**
 - Previously published papers will not be considered; this includes papers appearing in published workshop proceedings. Papers presented only orally, or only available via informal distribution at a workshop or meeting can be submitted for publication at ICCAD.
 - Authors should clearly address the significance of their contribution as part of the paper.

AUTHOR'S SCHEDULE

Deadline for submissions: April 16, 2003
Notification of acceptance: June 27, 2003
Deadline for final version: August 11, 2003

Papers will not be accepted for submission after 5:00 PM Mountain Daylight Time, April 16, 2003. This deadline is firm and inflexible. No exceptions will be made.

Please direct all correspondence to:

ICCAD Publications Department

MP Associates, Inc. Telephone: (303) 530-4562
 5305 Spine Rd., Suite A Fax: (303) 530-4334
 Boulder, CO 80301 Email: terri@mpassociates.com

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