

ASP-DAC 2000 General Chair's Message

Welcome to ASP-DAC 2000

On behalf of the Organizing Committee, I would like to welcome you to the Asia and South Pacific Design Automation Conference 2000 (ASP-DAC 2000). This year's ASP-DAC will be held again in the Pacifico Yokohama, Japan, jointly with EDA TechnoFair 2000, premier Japanese EDA exhibition. The goal of the ASP-DAC is to provide an international forum for researchers and engineers in academia and industry, in the area of electronic system/VLSI design, and DA/CAD. ASP-DAC is a sister conference of DAC in the USA and DATE in Europe.



Three keynote speakers will talk about future directions of VLSI design technologies from different points of view. Dr. Shojiro Asai, Corporate Officer & President of Hitachi R & D Group will talk about the impact of communications convergence on VLSI. Prof. C. L. Liu, President of National Tsing Hua University, Taiwan, will talk about education, design and fabrication technologies. And, finally, Mr. Willam Herrick, Director of Compaq Alpha Development Group, USA, will talk about challenges in multi-GHz processor designs.

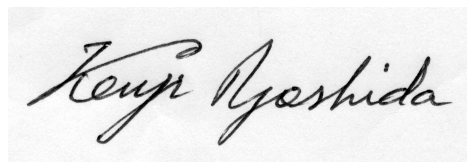
An excellent technical program has been organized by the committee co-chaired by Prof. H. Kunieda and Prof. C.M. Kyung, and with voluntary work of numerous experts from worldwide. This year we have an increased number of paper submissions in a wide spectrum of technical areas, and also from variety of geographical regions. 90 papers selected have been organized into five tracks; LSI Design, System DA, Logic and Test, Physical DA, and Circuit and Device CAD. Each of them also includes special sessions, panels, embedded tutorials or invited talks by distinguished speakers. Among panels, a panel by EDA vendor executive is planned just after the opening session on Wednesday. They will talk on their strategies to solve the next generation design challenges.

A unique feature of ASP-DAC is the University LSI Design Contest, which focuses a real chip design in academia. 18 designs have been selected for poster presentation, from which 3 or 4 award winners will be selected at the conference.

On Tuesday, full day tutorials are scheduled to give complete introductions of five state-of-the-art design/CAD topics; (1) Embedded Microprocessor Design, (2) Power Reduction Techniques, (3) Test Techniques, (4) Front-End Optimization and Verification, and (5) Ultra Deep Submicron Design and Analysis.

Our community is now facing tremendous challenges caused by accelerated scaling down and ever increasing complexity of System-on-a-Chip. I believe the ASP-DAC 2000 will be a precious opportunity for you to find many hints for your solution by meeting, talking and exchanging ideas with many leading edge researchers, or friends of yours, in the same, or different, technical areas.

I hope you will have a valuable and enjoyable experience at Yokohama, Japan, in next January.

A handwritten signature in black ink, reading "Kenji Yoshida". The signature is written in a cursive, flowing style.

Kenji Yoshida
General Chair, ASP-DAC 2000