

ASP-DAC 2000 University LSI Design Contest Summary

Submission

Nineteen designs were submitted from four countries, Japan, Korea, China and Taiwan. Design area and methodologies are as follows. This year, most of design used full custom or cell based design methodology.

Country	Number of Submission	Application Area			Design Methodology		
		A	M	C	F/C	G/A	F/P
Japan	9	4	4	1	9	0	0
China	5	0	1	4	4	0	1
Korea	3	0	1	2	3	0	0
Taiwan	2	0	0	2	2	0	0

Area

A: Analog or A/D Mixed, M: Microprocessor or DSP, C: Custom or Application Specific

Methodology

G/A: Gate Array, F/P: FPGA/PLD

F/C: Full Custom/ Cell Based, G/A: Gate Array, F/P: FPGA/PLD

Paper Selection

Submitted designs were subjected to the reviewing by committee members who were classified into three groups corresponding areas: Analog or A/D Mixed, Microprocessor or DSP, and Custom or Application Specific. Each committee member was requested to fill in the review sheet, including the following criteria, rank and comments. (1) Reliability of design and implementation, (2) Quality of Implementation, (3) Performance of the Design, (4) Novelty of Application, Algorithm, Architecture, and Circuit configuration, (5) Additional Points, such as new design methodology and testability. Unlike usual reviews of technical papers, our evaluation of submitted placed more importance on how the LSI circuits were designed and implemented than anything else. Design selection was carried out the committee meeting held on September 21. Discussion on the selection rejection of each design was carried out, based on the recommendation points and comments. As a result, 18 designs are selected and are presented in these proceedings. One design was rejected, since it is too specific to interest the conference. A candidate for the Outstanding Design Award, and two candidates for Special Feature Awards were selected.