

University LSI Design Contest Co-Chairs' Message

The University LSI Design Contest was born a unique program of ASP-DAC Conference. The purpose of the Contest is to encourage education and research on LSI design and its implementation into chips at universities, and other educational organizations, by providing the opportunities to present and discuss their latest designs at the conference.



Ryota Kasai



Annatha Chandrakasan

Application areas and types of circuits include (1) Analog and Mixed-Signal Circuits, (2) Digital Signal Processing, (3) Microprocessors, and (4) Custom Application Specific Circuits. Methods, or technology used for implementation include (a) Full Custom and Cell-Based LSIs, (b) Gate Arrays, and (c) Field Programmable Devices, including FPGA/PLDs. Actually implemented on real chips is required of all the designs.



Hideharu Anamo

This year, eighteen selected designs from four countries will be disclosed in Session A1, with a short aural presentations followed by Q & A, using posters. Three out of the eighteen are with analog and mixed signal, six are with microprocessors, and nine are with custom application specific circuits. Demonstrations on the achievements will also be made for some designs. Opportunities for demonstrations at EDA TechnoFair2000 will be provided for the above designs.

Submitted designs were reviewed by the members of the University Design Contest Committee, based on the following criteria: Reliability of design and implementation, Quality of implementation, Performance of the design, Novelty, and Additional factors. In the selection process, emphasis was placed more on reliability, quality, and performance, rather than on novelty of designs. AS a result, the eighteen designs were selected. And also, a certain number of awards will be given to outstanding designs, selected from those presented at the conference.

It is our great pleasure if the design contest will contribute to the promotion of research and education in LSI design field at academic organizations. It is also our hope that many people not only in academia but in industry will attend the contest and enjoy stimulating discussion.

Ryotakasai Anantha padmanabhan

Ryota Kasai and Anantha Chandrakasan
Co-Chairs, University LSI Design Contest Committee

天野英晴

Hideharu Amano
Vice Chair, University LSI Design Contest Committee