University LSI Design Contest

The University LSI Design Contest was conceived as a unique program of ASP-DAC Conference. The purpose of the Contest is to encourage education and research in LSI design, and its realization on chips at universities, and other educational organizations by providing opportunities to present and discuss innovative and state-of-the-art designs at the conference. Application areas and types of circuits include (1) Analog, RF and Mixed-Signal Circuits, (2) Digital Signal Processing, (3) Microprocessors, and (4) Custom Application Specific Circuits and



Memories. Methods or technologies used for implementation includes (a) Full Custom and Cell-Based LSIs, (b) Gate Arrays, and (c) Field Programmable Devices, including FPGA/PLDs.

This year, thirteen designs will be disclosed in Session 1D with short presentations followed by live discussions in front of posters. Submitted designs were reviewed by the members of the University Design Contest Committee based on the following criteria: Reliability of the design and implementation, Quality of the implementation, Performance of the design, Novelty, and Additional special features. In the selection process, emphasis was placed more on reliability, quality, and performance. As a result, the thirteen designs were selected. In addition, we have instituted one outstanding design award and one special feature award.

It is with great pleasure that we acknowledge the contributions to the Design Contest, and it is our earnest belief that it will promote and enhance research and education in LSI design in academic organizations. It is also our hope that many people not only in academia but also in industry will attend the contest and enjoy the stimulating discussions.

Date, Time, and Locations:

 Oral Presentation
 10:15 - 11:55, January 22, 2008, Room 311BC

 Poster Presentation
 12:20 - 13:30, January 22, 2008, Room 321A

University LSI Design Contest Committee Chair In-Cheol Park (*KAIST, Korea*)

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