Message from Technical Program Committee

On behalf of the Technical Program Committee for the Asia and South Pacific Design Automation Conference 2006, we would like to welcome all of you to the conference held from January 24 through 27, 2006 at Pacifico Yokohama Conference Center in Yokohama, Japan.

Hidetoshi Onodera   Yusuke Matsunaga

This year, ASPDAC received 424 paper submissions, which is the second highest number in our history. This is a 46% increase in submissions over the last ASPDAC held in Japan two years ago. The submissions span 27 countries/regions in Asia, North America, South America, Europe, Oceania, and Africa.

The Technical Program Committee was composed of 64 professionals who are experts on EDA, LSI design, and embedded system design, and was organized into 11 sub-committees. The number of committee members is relatively small compared to the previous conferences. However, all the members were committed to make in-depth reviews for all the papers assigned to each sub-committee and physically attend the TPC meeting for paper selection. Based on the result of a rigorous and thorough review followed by a full day face-to-face discussion, we have selected 135 papers and compiled them into a 3-day program of 27 technical sessions in three parallel tracks. The technical program is further enriched by multiple special sessions and panels in one more track, resulting in four parallel tracks of exciting presentations and discussions.

Each day, the technical program starts with a keynote address that is organized through the leadership of Fumiyasu Hirose, General Chair, followed by regular and special sessions. We have 7 special sessions in total. On Wednesday, selected submissions to the University Design Contest will be presented at Session 1D. Session 2D focuses on electro-thermal design and Session 3D discusses flash memory in embedded systems. On Thursday, Session 4D addresses an overview of Open Access and Session 5D focuses on low power design challenges for mobile applications. On Friday, Session 7D discusses H.264 design issues and Session 8D addresses design methodology for "Cell" processor development. It should be mentioned that Sessions 5D and 8D are organized as Designers' Forum that is a brand new event for encouraging mutual exchange between/within designers in industry and EDA researchers. Besides two special sessions, Designers' Forum will organize two panel discussions on functional verification (Session 6D, Thursday) and top-10 design issues (Session 9D, Friday). These special sessions will provide you with a wide variety of hot and exciting topics from system level down to physical level, and from industrial design issues to theoretical fundamentals.

On behalf of the Technical Program Committee, we would like to thank all people involved in the 2006 event. In particular, we would like to thank the members of the Organizing and Technical Program Committees, the members of Industry Liaison, special session organizers, and everyone at JESA for conference management. Also, we would like to express our sincere thanks to all the authors who submitted papers with valuable results, since their contributions form the basis of our technical excellence.

We would be more than happy if you could attend the conference and find something new

in the directions of EDA and technologies during ASP-DAC 2006.

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